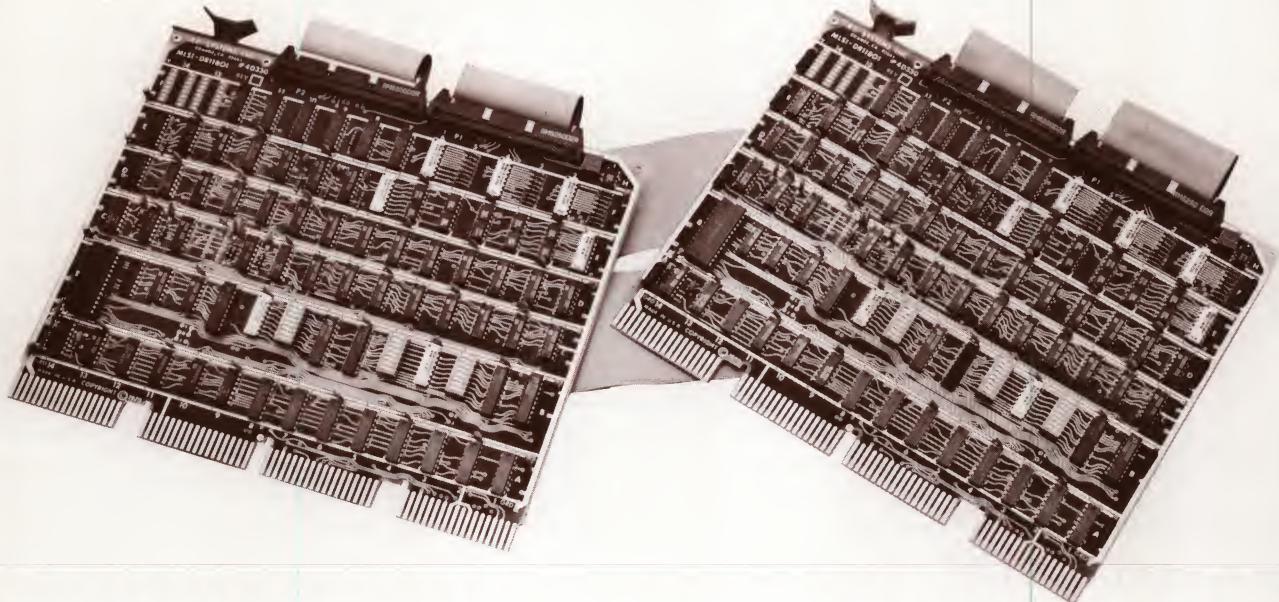


# MDB

INTERPROCESSOR  
LINK SUBSYSTEMS  
for use with  
**LSI\*-11 and 11/23**  
Computers



## MDB Parallel DMA Interprocessor Link between two LSI-11/2 or 11/23 Computers, optically isolated

- Compatible with DEC DR11B and DA11B operating and diagnostic software
- Differential driver and optically isolated receiver circuits provide high-speed parallel data transfers at distances up to 1000 feet
- Optical Isolation provides AC/DC circuit isolation (3KV) and ground loop elimination
- Requires one QUAD slot of a standard LSI-11 backplane in each computer
- All cabling provided for operation between two computers up to 50 feet, optional lengths up to 1000 feet
- Selectable feature allows data transfers across 32K boundaries in blocks up to 32K words

- Selectable address, interrupt vector, and bus level provided, preset to DEC standard assignment of 77241X
- Adjustable data transfer rates for regulating DMA load on each computer

The MLSI-DA11BOI Interprocessor DMA Link provides a high-speed parallel data transfer channel between two LSI-11 computer systems with complete optical isolation between them. Through the use of differential drivers coupled with optical isolator receivers maximum AC and DC circuit isolation and ground loop current elimination is achieved while data transfer rates up to 500,000 words per second are maintained. The Interprocessor Link operates in a half-duplex mode with both computers transferring data via DMA control on a block basis. Data rate adjustments are provided for optimum system operation depending on system configuration. The MLSI-DA11BOI provides DEC DA11B compatibility in one quad slot of a standard DEC backplane (MLS-

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BPA84), presents only one unit load to the Q-bus, and optically isolates one computer system from the other.

#### ELECTRICAL REQUIREMENTS:

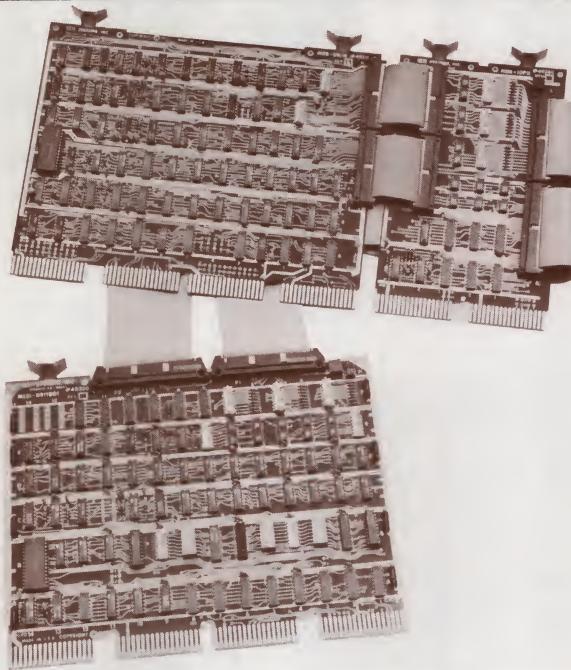
- +5V at 2.6A

#### ACCESSORIES:

- Optional cable lengths beyond 50 feet

#### PHYSICAL:

- Occupies one quad slot of standard DEC backplane, MLSI-BPA84 or equivalent



## MDB Parallel DMA Interprocessor Link between a PDP\*-11 and LSI-11/2 or 11/23 Computer, optically isolated

- Compatible with DEC DR11B and DA11B operating and diagnostic software
- Differential driver and optically isolated receiver modules provide high-speed parallel data transfers at distances up to 1000 feet
- Optical Isolation provides AC/DC circuit isolation (3KV) and ground loop elimination
- Requires one Hex SPC slot of a standard DEC system unit in the PDP-11 and a quad slot of standard backplane in the LSI-11 computer
- All cabling provided for operation between two computers up to 50 feet, optional lengths up to 1000 feet
- Selectable feature allows data transfers across 32K boundaries in blocks up to 32K words
- Standard MDB-DR11B may be disconnected from differential driver/optical receiver module for fault isolation and trouble shooting
- Selectable address, interrupt vector, and bus level provided, preset to DEC standard assignment of 77241X
- Adjustable data transfer rates for regulating

#### NPR (DMA) load on each computer

The MDB/MLSI-DA11BOI Interprocessor DMA Link provides a high-speed parallel data transfer channel between LSI-11 and PDP-11 computer systems with complete optical isolation between them. Through the use of differential drivers coupled with optical isolator receivers maximum AC and DC circuit isolation and ground loop current elimination is achieved while data transfer rates up to 500,000 words per second are maintained. The Interprocessor Link operates in a half-duplex mode with both computers transferring data via NPR (DMA) control on a block basis.

Data rate adjustments are provided for optimum system operation depending on system configuration. The MDB/MLSI-DA11BOI provides DEC DA11B compatibility in one-quarter of the system space in the PDP-11, requires only one quad slot in the LSI-11 computer, presents only one unit load to the Unibus\* and Q-bus, and optically isolates one computer system from the other.

#### ELECTRICAL REQUIREMENTS:

- +5V at 2.6A (each computer)

#### ACCESSORIES:

- Optional cable lengths beyond 50 feet

#### PHYSICAL:

- Occupies one hex slot of the standard PDP system unit
- Occupies one quad slot of a standard LSI DEC backplane or MLSI-BPA84

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